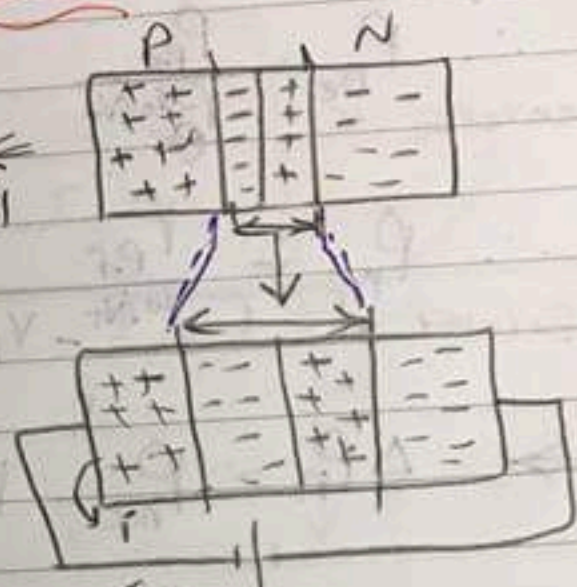
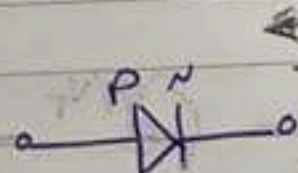


## \* PN junction under Reverse Bias :-

في حالة ال Reverse يتجاذب مع بعض  
الايون مع سالبة الكاربي  
ويتجاذب سالبة الايون مع موجبة الكاربي  
فتزداد مساحة ال Deplation Region  
فتبدو وكأنها open-circuit "دائرة مفتوحة"  
تتدفق مرور التيار ويكاد ينعدم.

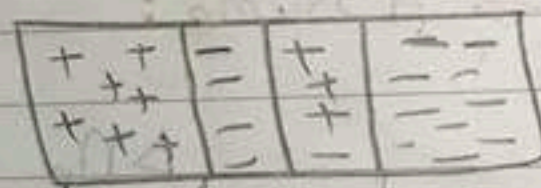


$$\Rightarrow G = \frac{C}{\sqrt{1 + \frac{V_R}{V_0}}}$$



## \* Forward Bias :

في حالة ال Forward  
يحدث تناقص في P-type و ال N-type  
فتتكون مساحة ال Deplation Region  
فتبدو وكأنها "s.c" "سلك قصير".



$$V_0 = \frac{kT}{q} \ln \frac{P_p}{P_n} \Rightarrow \text{at (P-side)}$$

$$\Rightarrow \therefore \frac{kT}{q} = V_T \rightarrow \text{thermal}$$

$$V_0 = V_T \ln \frac{P_p}{P_n}$$

$$\frac{V_0}{V_T} = \ln \frac{P_p}{P_n}$$



$$\therefore \frac{v_0/V_T}{e} = \frac{P_p}{P_n} \Rightarrow \therefore P_n = \frac{P_p}{e^{v_0/V_T}} \text{ at equilibrium}$$

في حالة التوازن

$$\therefore P_{ne} = \frac{P_{pe}}{e^{v_0/V_T}} \rightarrow (1)$$

reverse

$$\therefore P_{nf} = \frac{P_{pf}}{(e^{v_0/V_T} - 1)} \rightarrow (2)$$

forward

$$\Rightarrow \Delta P_n = P_{nf} - P_{ne} = \left[ \frac{P_{pf}}{(e^{v_0/V_T} - 1)} - \frac{P_{pe}}{e^{v_0/V_T}} \right]$$

$$\therefore \Delta P_n = \frac{N_A}{e^{v_0/V_T}} (e^{v_0/V_T} - 1) \rightarrow *$$

$$\Rightarrow \text{Assume: } P_{pe} = P_{pf} = N_A$$

$$\therefore \Delta n_p = \frac{N_D}{e^{v_0/V_T}} (e^{v_0/V_T} - 1) \rightarrow **$$

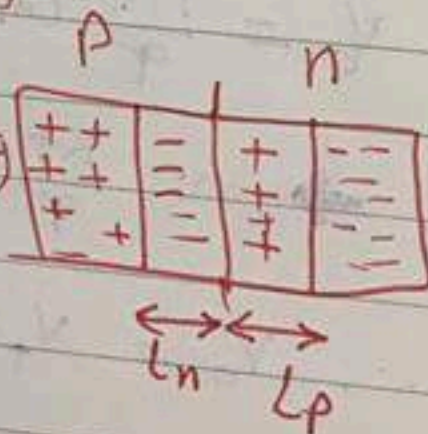
$$\therefore I_{\text{Tot}} \propto (\Delta P_n + \Delta n_p) \therefore I \propto \frac{N_A}{e^{v_0/V_T}} (e^{v_0/V_T} - 1) + \frac{N_D}{e^{v_0/V_T}} (e^{v_0/V_T} - 1)$$

$$\therefore I_s = A q n_i^2 \left[ \frac{D_n}{N_A L_n} + \frac{D_p}{N_D L_p} \right]$$

$\downarrow$  n.p  
 $\downarrow$  P-type  
 $\downarrow$  n.type

$$\therefore I_t = I_s (e^{v_F/V_T} - 1)$$

$\rightarrow L_p$  is the diffusion of p-type in n-type  
 $\rightarrow L_n$  " " " of n-type in p-type





$$* I_T = I_S (e^{V_F/V_T} - 1)$$

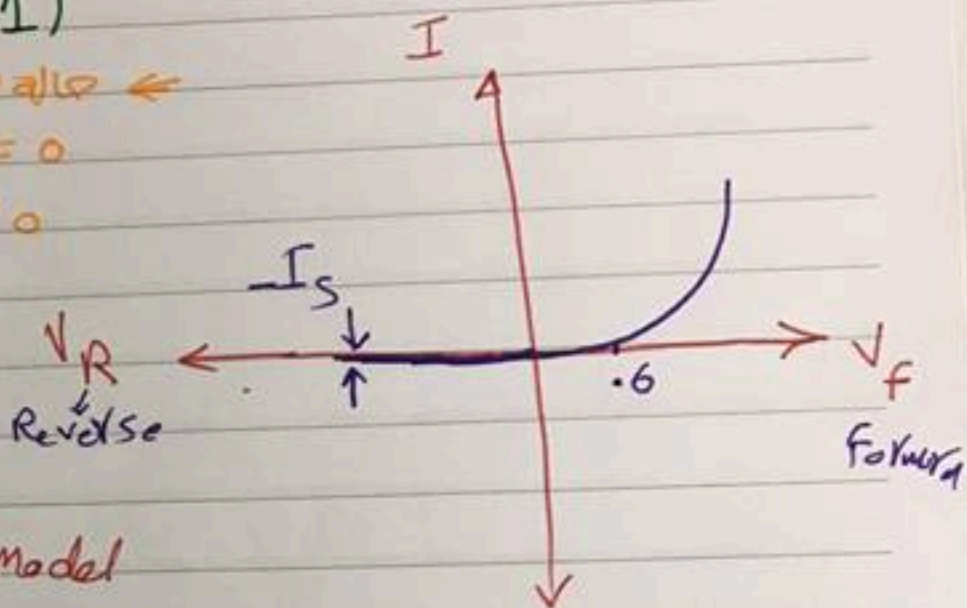
→ at  $V_F = 0 \Rightarrow I = 0$

→ at  $V_F = 0.6 \Rightarrow I \approx 0$

$V_F$   
= 0.6      = 0

Constant model.

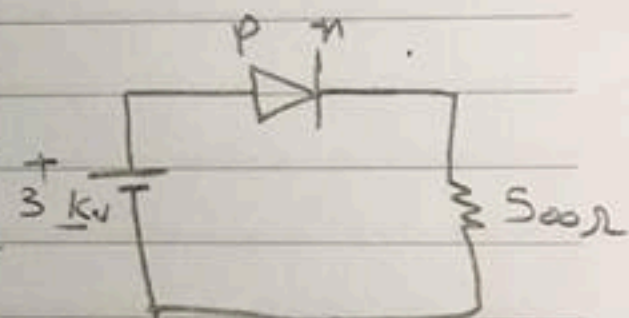
ideal model



ex:-

connected with p-type

p-n junction under forward bias

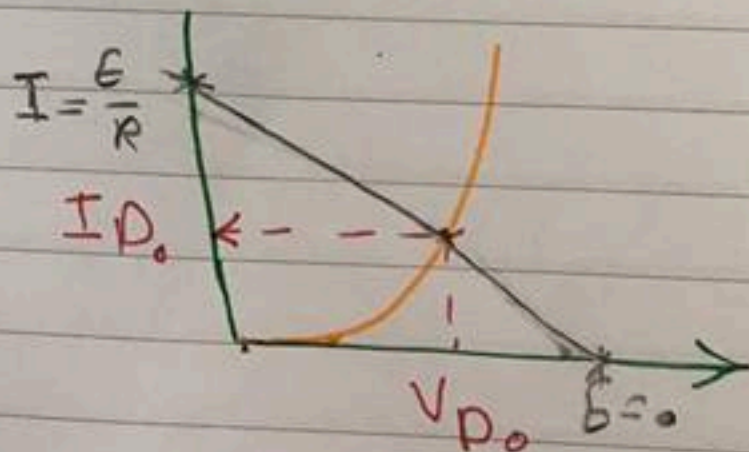


$$\Rightarrow E = V_D + I_D \cdot R$$

at  $V_D = 0 \quad \therefore E = I_D \cdot R \quad \therefore I_D = \frac{3 \times 10^3}{500}$

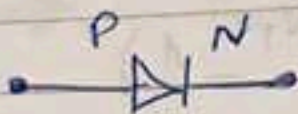
$\Rightarrow$  at  $I_D = 0 \quad \therefore E = V_D$

$\Rightarrow \frac{V_{D0}}{I_{D0}} = R_{D0}$  "static Resistance" static Resistance





6 sep 20



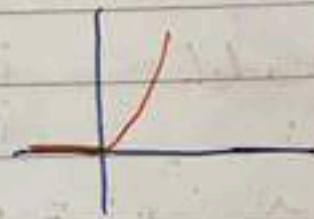
① Ideal

② Constant Voltage

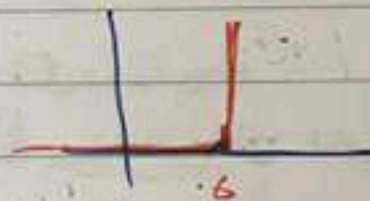
③ exponential

①  $\Rightarrow I_D = I_S (e^{V_D/kT} - 1)$

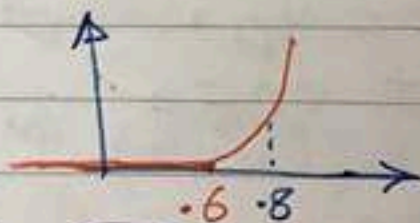
$I_D = I_S (\exp \frac{V_D}{kT} - 1)$



②  $I_D = I_S (e^{V_D/kT} - 1)$



③ exponential



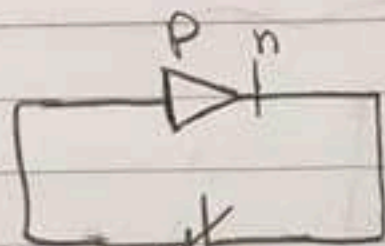
$$I_S = A q n_i^2 \left( \frac{D_n}{N_A L_n} + \frac{D_p}{N_D L_p} \right)$$

As  $I_S$  is very small



نوع صند أنواع  
الدايود

## Zener Break down :-



↑  $V_D$  يزداد  
Doping أقل

Reverse B.A

الحجم العكسي يزداد

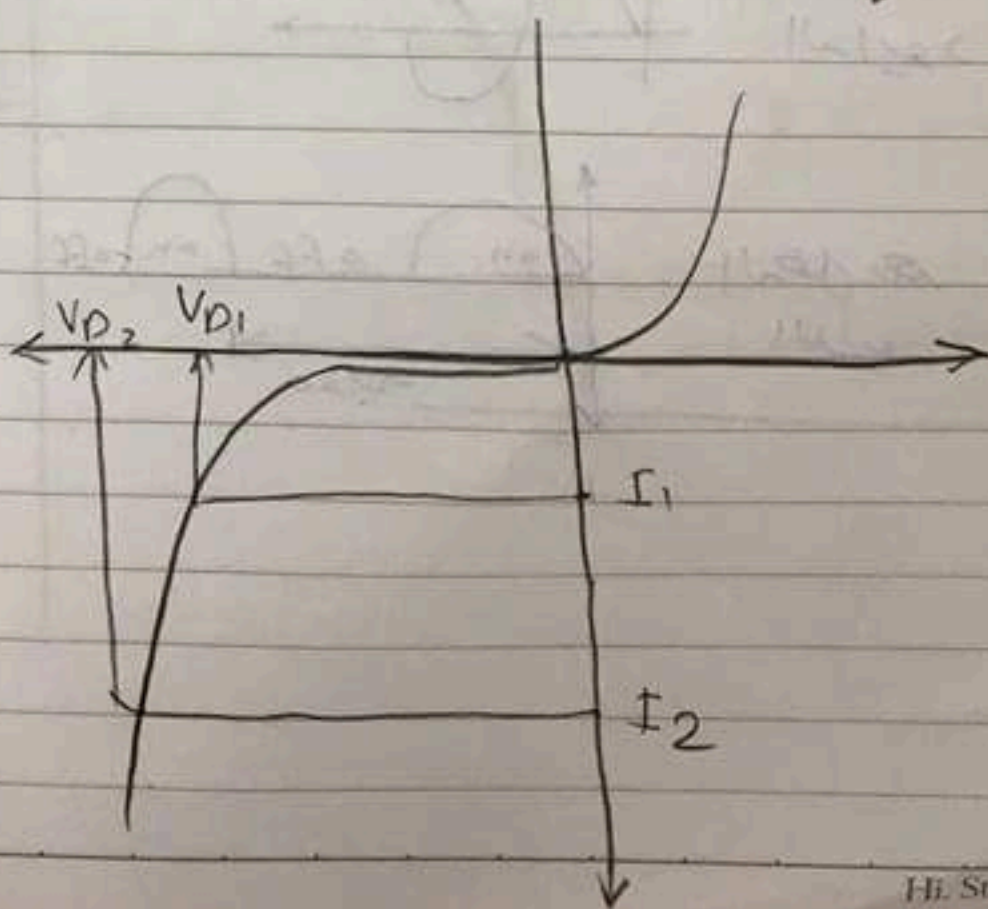
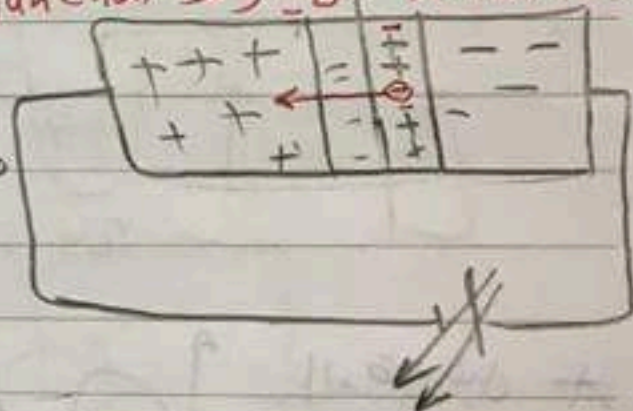
الطبيعة عكس، عند دايود ال Doping يتأثر بنسبة  
معدن - العنصرية ... تثبيت الفولت  
وزيادة حصة التيار

\* Complete failure.

لا فرق في الحجم العكسي يزداد جداً وكسب الإلكترونات - طاقة حرارية كبيرة  
فتبدأ مع فجوات P-type ... فيحدث انهيار لل Junction  
Breakdown انهيار لل Junction. وسبب هذا

## AVALANCH RD

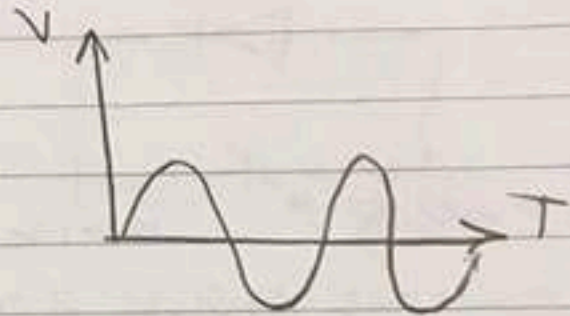
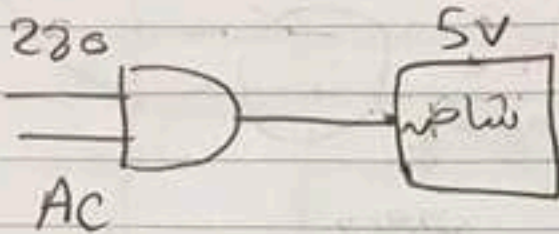
وهذا يحدث عند Doping أقل  
و فرق جهد عاليه .



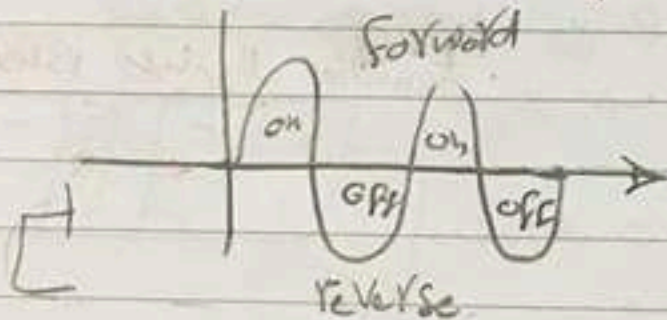


## CH 2 :-

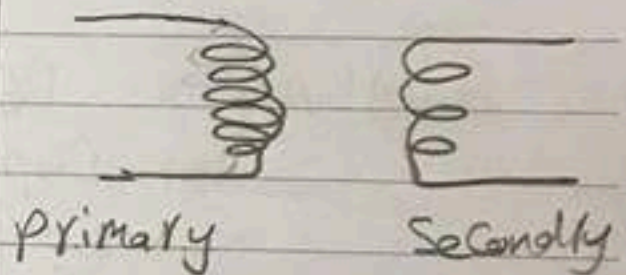
### Diode Models and Application



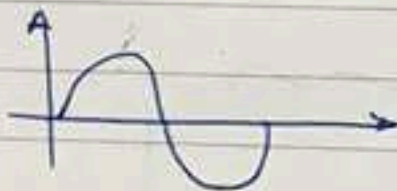
Diode as Switch  $\rightarrow$  on  $\rightarrow$  closed  
 $\rightarrow$  off  $\rightarrow$  open



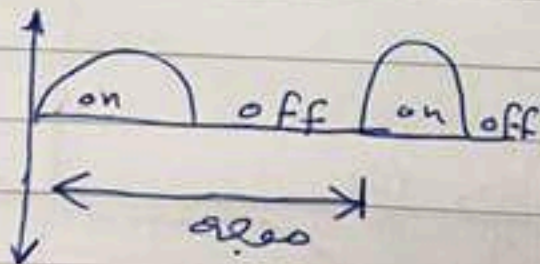
\* Step down transformer



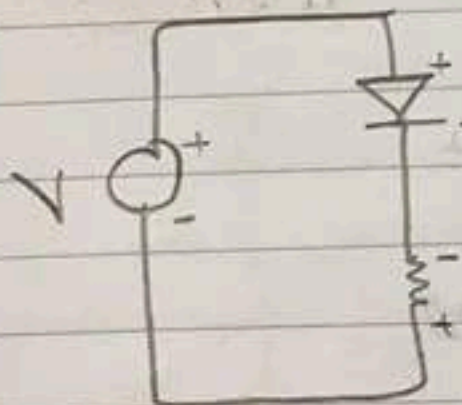
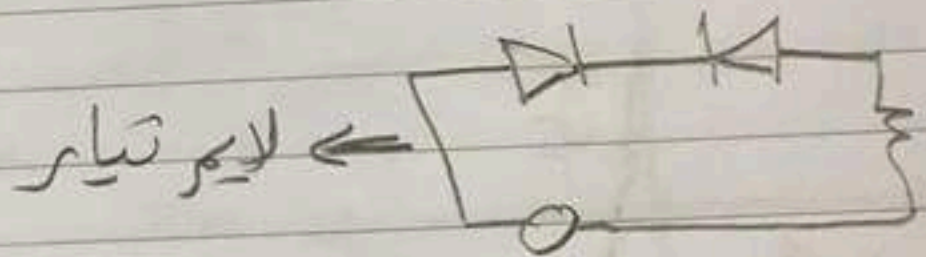
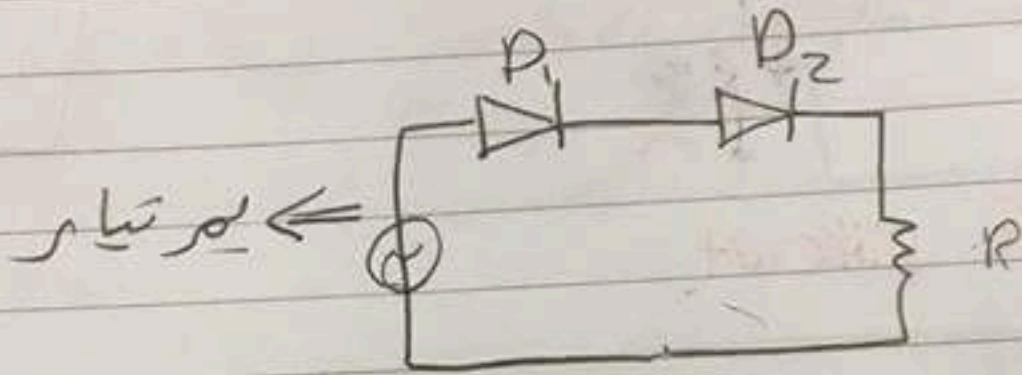
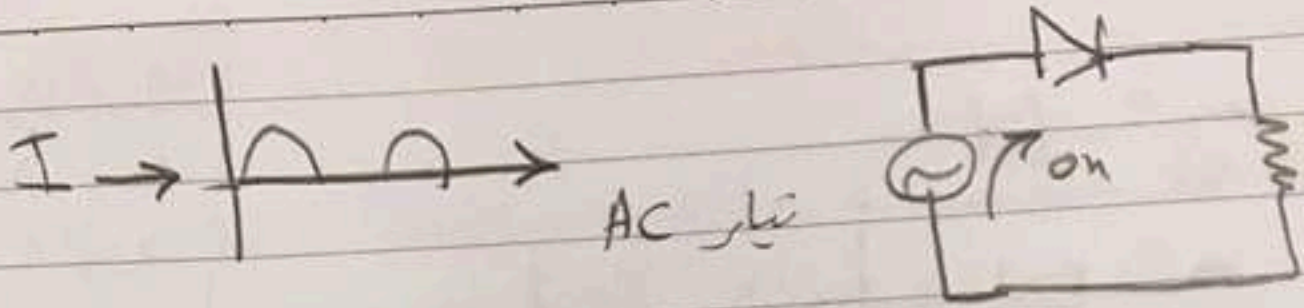
\* الرقبة قبل  
الدايود



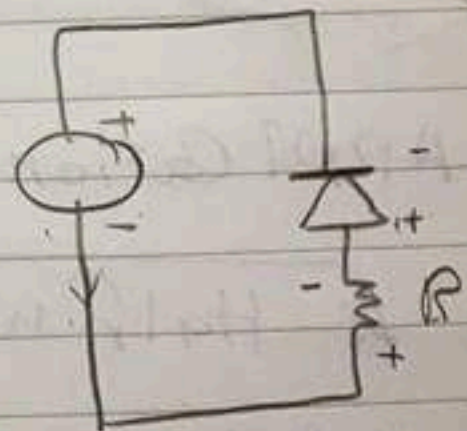
\* الرقبة بعد  
الدايود







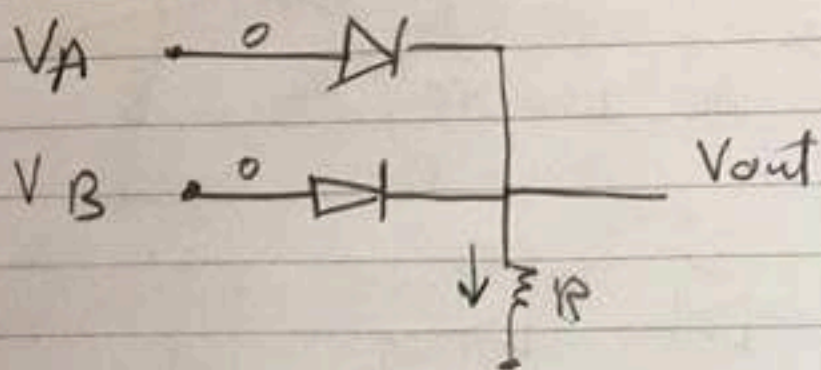
Forward



Reverse

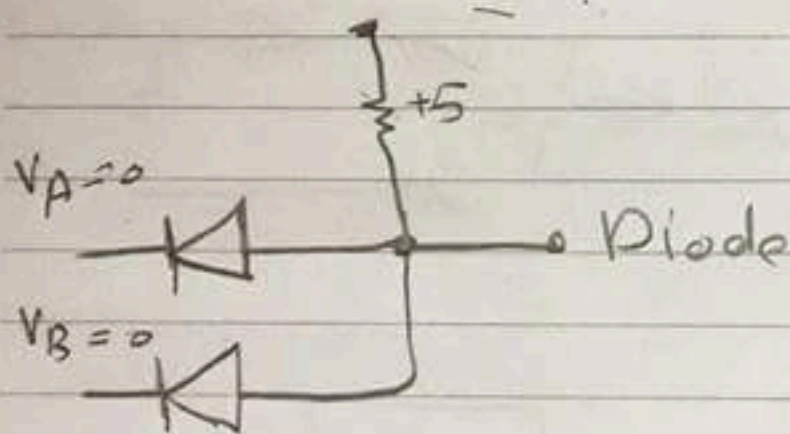
الاتجاه العكسي للتيار





$V_A$	$V_B$	$V_{out}$
0	0	0
0	1	1
1	0	1
1	1	1

OR Circuit



$V_A$	$V_B$	$V_{out}$
0	0	0
0	1	0
1	0	0
1	1	1

AND Circuit

Application of Diode:

(i) Half wave Rectifier

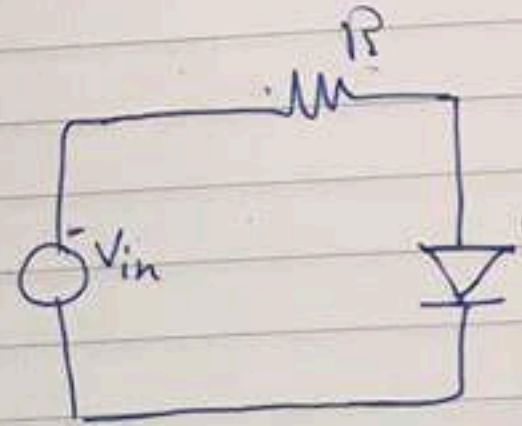
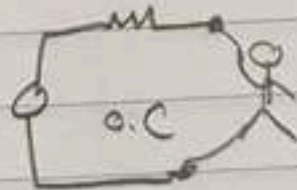
\* Diode  $\Rightarrow$  OR, AND



## Resistor Diode Circuits

at  $V_{in} < 0$  Diode forward  
open circuit

$$V_{in} = V_{out}$$



at  $V_{in} > 0$

ideal ①  $V_D = 0$

②  $V_D = -0.6$

①  $\Rightarrow$  Diode forward on

$$\therefore V_D = 0 = V_o$$



$V_{in}$

$V_{in}$

$V_{out}$

②  $V_D = -0.6$

